

Spindle Speed control through +/-10V DAC channel (ET10)

ET10 control board contains 6 channel +/-10V 12bits DAC outputs. The outputs normally controlled through PID and used for closed-loop analogue servo drivers control. However if PID is disabled, +/-10V DAC output can be used as general purpose DAC.

Output level of DAC output can be controlled through "DAC Offset" register.

ET10BB breakout board contains inverted operational amplifier, so zero (0) value in "DAC Offset" register corresponds to +10V output voltage level, maximum value (0x3ff) corresponds to -10V voltage level.

There are number of ways to access to DAC Offset register.

PLC controller (Hardware)

!!!! Please check if the DACs are not in the test mode before using. !!!

DAC offset register is mapped to address 0x32 of [Extern CNC Variables](#) Area.

```
#define EXT_ET5_DAC_OFFSET 0x32
```

16 bits value should be written to this register. low 12 bits is DAC value to be written high 4 bits is DAC channel

Message PLCCMD_SET_CNC_EXTVAR should be sent to myCNC control core from PLC to get access [Extern CNC Variables](#), then 2 ms delay should be initiated to push the message from PLC to myCNC Core.

```
#define PLCCMD_SET_CNC_EXTVAR 1020
```

Check DAC Voltage programming PLC code example

```
dac_value=555;                                     //DAC value to be sent

message=PLCCMD_SET_CNC_EXTVAR;                       //setup message variable to
PLC-to-CORE command

command=0x32; //EXT_ET5_DAC_OFFSET                   //setup command register
(var00) for DAC register access

parameter=dac_value+(2<<12);                         //setup parameter register
(var01), DAC channel is #2

texit=timer+2;do{timer++;}while(timer<texit); //2ms delay ti push the
message
```

Another examples with +/-10V DAC programming can be found here - [M03.plc](#), [SPN.plc](#)

Configuration dialogs

Settings->Cfg->Hardware->Analogue Closed Loop Dialog.

Each DAC outputs can be accessed by changing values in "DAC Offset" spinboxes.

Enable	PID, K	PID, K0	PID, Kp/1024	PID, Kd/1024	PID, Ki/8192	Integral Limit	Encoder channel	Dead Zone	DAC offset
Motor#0	-70	<<	0	0	30	3000	2	0	2115
Motor#1	-69	<<	0	0	-10	3000	1	8	240
Motor#2	-70	<<	0	0	-3	3000	0	0	2115
Motor#3		<<					3	0	73
Motor#4		<<					4	0	21
Motor#5	135	<<	0	150	0		3	0	2115

Position Err: #0 [x] green, #1 [x] green, #2 [] red, #3 [] white, #4 [] white, #5 [] white
Speed Control: #0 [x] red, #1 [x] red, #2 [] white, #3 [] white, #4 [] white, #5 [] white
Sampling Rate: 20

From:
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Permanent link:
http://cnc42.com/spindle_speed_control_through_-10v_dac_channel_et10

Last update: 2018/08/31 10:00

